Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	38	(layout same schematic) same marker	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/07 06:26
L2	35	(layout same schematic) same ((wire or line) adj width)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/08/07 06:26
L3	38	(layout same schematic) and (((wire or line) adj width) same ((minimum or smallest) near10 (larger or bigger or greater)))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/07 06:27
L4	47	(layout same schematic) and (((wire or line) adj width) same (flag\$4 or mark\$4))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/07 06:27
L5	32	(layout same schematic) and (((wire or line) adj width) same ((minimum or smallest) near10 (larger or bigger or greater)))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/08/07 06:28
L6	11	schematic near10 (((wire or line) near2 width) near2 (minimum or smallest))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/07 06:28
L7	35	(layout same schematic) same ((wire or line) adj width)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/08/07 06:29
L8	2	(schematic with line with width with marker with minimum).clm.	US-PGPUB	OR	ON	2006/08/07 06:30
L10	890	716/11.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/08/07 06:31

		LASI Seal		,		
S1	13	(layout same schematic) same marker	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/14 13:05
S2	6	(layout same schematic) same ((wire or line) adj width)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2003/10/20 09:14
S3	61152	(minimum or smallest) near10 (larger or bigger or greater)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/10/16 11:08
S4	6	(layout same schematic) and (((wire or line) adj width) same ((minimum or smallest) near10 (larger or bigger or greater)))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/03/25 15:00
S5	8	layout same (schematic near5 (mark\$4 or flag\$4))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/10/16 11:10
S6	319	(layout same schematic) and ((wire or line) adj width)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2003/10/20 09:14
S7	12	(layout same schematic) and (((wire or line) adj width) same (flag\$4 or mark\$4))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/10/20 09:15
S8	11156	layout same schematic	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/10/20 09:19
S9	257	((wire or line) adj width) same ((minimum or smallest) near10 (larger or bigger or greater))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/10/20 09:19
S10	11	(layout same schematic) and (((wire or line) adj width) same ((minimum or smallest) near10 (larger or bigger or greater)))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2003/10/20 09:21

		LASI Scare		,		
S11	17	(((wire or line) adj width) same ((minimum or smallest) near10 (larger or bigger or greater))) and electromigration	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/10/20 09:25
S12	112	"716"/\$.ccls. and electromigration	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/10/20 09:25
S13	23	"716"/\$.ccls. and electromigration and (wire adj width)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2003/10/20 09:26
S14	698529	LVS	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/03/25 15:17
S15	8908	layout near5 schematic	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/03/25 15:09
S16	707037	LVS or (layout near5 schematic)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/03/25 13:05
S17	503	"716"/\$.ccls. and (LVS or (layout near5 schematic))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/03/25 14:48
S18	32108	(wire or line) adj width	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/03/25 14:48
S19	54	("716"/\$.ccls. and (LVS or (layout near5 schematic))) and ((wire or line) adj width)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/03/25 13:07
S20	142	"716"/\$.ccls. and LVS	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/03/25 14:48

		LASI Sear	,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,			
S21	52478	(wire or line) near2 width	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/03/25 14:48
S22	64927	(wire or line) near2 width	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/03/25 14:48
S23	44	("716"/\$.ccls. and LVS) and ((wire or line) near2 width)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/03/25 14:48
S24	278	(((wire or line) adj width) same ((minimum or smallest) near10 (larger or bigger or greater)))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/03/25 15:26
S25	4	LVS and ((((wire or line) adj width) same ((minimum or smallest) near10 (larger or bigger or greater))))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/03/25 15:03
S26	7	"6038383"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/03/25 15:03
S27	3049	((wire or line) near2 width) near2 (minimum or smallest)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/03/25 15:16
S28	5	schematic near10 (((wire or line) near2 width) near2 (minimum or smallest))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/03/25 15:09
S29	174	(((wire or line) near2 width) near2 (minimum or smallest)) and "716"/\$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/03/25 15:16
S30	15	((((wire or line) near2 width) near2 (minimum or smallest)) and "716"/\$.ccls.) and LVS	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/03/25 15:16

		LASI Scare		,		
S31	99	LVS and drc	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/03/25 15:18
S32	14	(((wire or line) near2 width) near2 (minimum or smallest)) and (LVS and drc)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/03/25 15:18
S33	10	(((wire or line) adj width) near5 (minimum or smallest)) same ((design near2 rule) near5 (larger or bigger or greater))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/03/25 15:47
S34	1893	"minimum line width"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/03/25 15:32
S35	61	"minimum line width" and "716"/\$. ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/03/25 15:32
S36	16	(((wire or line) adj width) near5 (minimum or smallest)) same ((design near2 rule) same power)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/03/25 15:44
S37	0	(((wire or line) adj width) near5 (minimum or smallest)) same resize	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/03/25 15:45
S38	0	(((wire or line) adj width) near5 (minimum or smallest)) near5 resize	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/03/25 15:45
S39	1	((wire or line) adj width) near5 resize	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/03/25 15:46
S40	661	((wire or line) adj width) near5 power	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/03/25 16:08

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S41	6	design near2 rule same (((wire or line) adj width) near5 power)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/03/31 13:59
S42	37	minimum near5 ((wire or line) adj width) near5 power	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/03/25 16:09
S43	32	((wire or line) adj width) near5 power near5 minimum	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/03/25 16:09
S44	10	design near2 rule same (((wire or line) adj (width or pitch)) near5 power)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/03/31 14:02
S45	1861	var\$6 near3 ((wire or line) adj (width or pitch))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/03/31 14:15
S46	1	(var\$6 near3 ((wire or line) adj (width or pitch))) same (LVS or (layout near5 schematic))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/03/31 14:06
S47	30	(var\$6 near3 ((wire or line) adj (width or pitch))) and (LVS or (layout near5 schematic))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/03/31 14:05
S48	41759	((wire or line) adj (width or pitch))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/03/31 14:07
S49	14	(((wire or line) adj (width or pitch))) same (LVS or (layout near5 schematic))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/03/31 14:07
S50	2	(var\$6 near3 ((wire or line) adj (width or pitch))) same DRC	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2004/03/31 14:15

		LASI Seal				
S51	29	DRC same ((wire or line) adj (width or pitch))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/03/31 14:16
S52	29	(layout same schematic) same marker	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/11 13:03
S53	22	(layout same schematic) same ((wire or line) adj width)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/11 13:03
S54	73621	(minimum or smallest) near10 (larger or bigger or greater)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/11 13:03
S55	28	(layout same schematic) and (((wire or line) adj width) same ((minimum or smallest) near10 (larger or bigger or greater)))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/11 13:03
S56	23	layout same (schematic near5 (mark\$4 or flag\$4))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/11 13:03
S57	34	(layout same schematic) and (((wire or line) adj width) same (flag\$4 or mark\$4))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/11 13:03
S58	14416	layout same schematic	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/11 13:03
S59	346	((wire or line) adj width) same ((minimum or smallest) near10 (larger or bigger or greater))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/11 13:03
S60	22	(layout same schematic) and (((wire or line) adj width) same ((minimum or smallest) near10 (larger or bigger or greater)))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/11 13:03

		EAST Scare		,		
S61	20	(((wire or line) adj width) same ((minimum or smallest) near10 (larger or bigger or greater))) and electromigration	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/11 13:03
S62	162	"716"/\$.ccls. and electromigration	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/11 13:03
S63	29	"716"/\$.ccls. and electromigration and (wire adj width)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/11 13:04
S64	856986	LVS	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/11 13:04
S65	10514	layout near5 schematic	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/11 13:04
S66	866967	LVS or (layout near5 schematic)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/11 13:04
S67	638	"716"/\$.ccls. and (LVS or (layout near5 schematic))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/11 13:04
S68	37355	(wire or line) adj width	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/11 13:04
S69	71	("716"/\$.ccls. and (LVS or (layout near5 schematic))) and ((wire or line) adj width)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/11 13:04
S70	207	"716"/\$.ccls. and LVS	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/11 13:04

		LASI Searc				
S71	64	("716"/\$.ccls. and LVS) and ((wire or line) near2 width)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/11 13:04
S72	12	LVS and ((((wire or line) adj width) same ((minimum or smallest) near10 (larger or bigger or greater))))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/11 13:05
S73	16	"6038383"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/11 13:05
S74	9	schematic near10 (((wire or line) near2 width) near2 (minimum or smallest))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/11 13:05
S75	30	((((wire or line) near2 width) near2 (minimum or smallest)) and "716"/\$.ccls.) and LVS	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/11 13:05
S76	16	(((wire or line) near2 width) near2 (minimum or smallest)) and (LVS and drc)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/11 13:05
S77	14	(((wire or line) adj width) near5 (minimum or smallest)) same ((design near2 rule) near5 (larger or bigger or greater))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/11 13:05
S78	97	"minimum line width" and "716"/\$. ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/11 13:05
S79	18	(((wire or line) adj width) near5 (minimum or smallest)) same ((design near2 rule) same power)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/11 13:06
S80	0	(((wire or line) adj width) near5 (minimum or smallest)) same resize	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/11 13:06

		LAST Scare		•		
S81	0	(((wire or line) adj width) near5 (minimum or smallest)) near5 resize	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/11 13:06
S82	1	((wire or line) adj width) near5 resize	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/11 13:06
S83	6	design near2 rule same (((wire or line) adj width) near5 power)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/11 13:06
S84	51	minimum near5 ((wire or line) adj width) near5 power	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/11 13:06
S85	46	((wire or line) adj width) near5 power near5 minimum	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/11 13:06
S86	11	design near2 rule same (((wire or line) adj (width or pitch)) near5 power)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/11 13:06
S87	9	(var\$6 near3 ((wire or line) adj (width or pitch))) same (LVS or (layout near5 schematic))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/11 13:07
S88	45	(var\$6 near3 ((wire or line) adj (width or pitch))) and (LVS or (layout near5 schematic))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/11 13:07
S89	32	(((wire or line) adj (width or pitch))) same (LVS or (layout near5 schematic))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/11 13:07
S90	10	(var\$6 near3 ((wire or line) adj (width or pitch))) same DRC	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/04/11 13:07

S91	44	DRC same ((wire or line) adj (width or pitch))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/11 13:07
S92	2	"6769103"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2005/04/14 13:05

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	8878	line with width with minimum	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/08/07 06:23
L2	149	layout and schematic and database and L1	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/08/07 06:23
L3	70	L2 and "716"/\$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/08/07 06:23
L4	413443	component same layer	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/08/07 06:23
L5	30	L3 and L4	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/08/07 06:23
L6	71521	schematic same layer	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/08/07 06:25
L7	41	L6 and L3	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/08/07 06:25
L8	56	schematic with layer with parameters	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/08/07 06:25
S1	187582	line with width	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/03/29 14:41
S2	1030	layout and schematic and database and S1	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/03/29 16:31
S3	173	S2 and "716"/\$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/03/29 14:41

S4	8471	line with width with minimum	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/03/29 14:41
S5	101	layout and schematic and database and S4	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/03/29 14:41
S6	64	S5 and "716"/\$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/03/29 16:28
S7	395714	component same layer	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/03/29 16:28
S8	27	S6 and S7	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/03/29 16:28
S9	67255	schematic same layer	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/03/29 16:34
S10	37	S9 and S6	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/03/29 16:31
S11	52	schematic with layer with parameters	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2006/03/29 16:34